FIG. 1

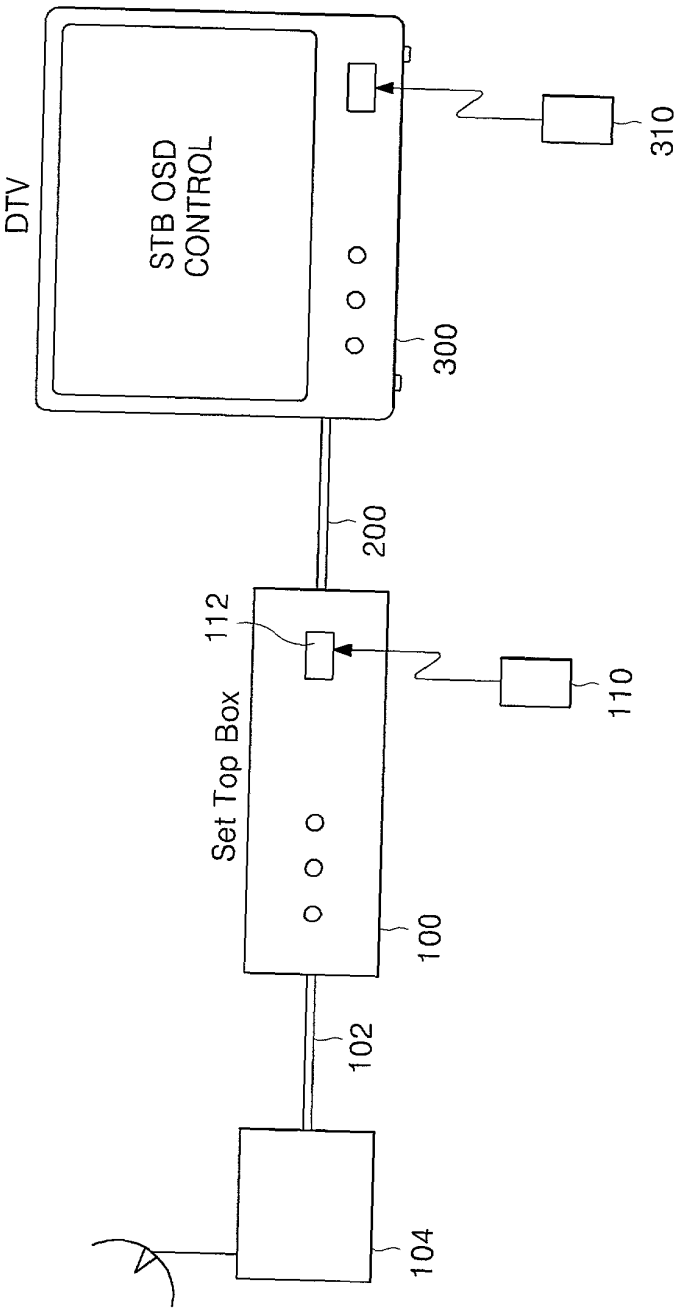

FIG. 2

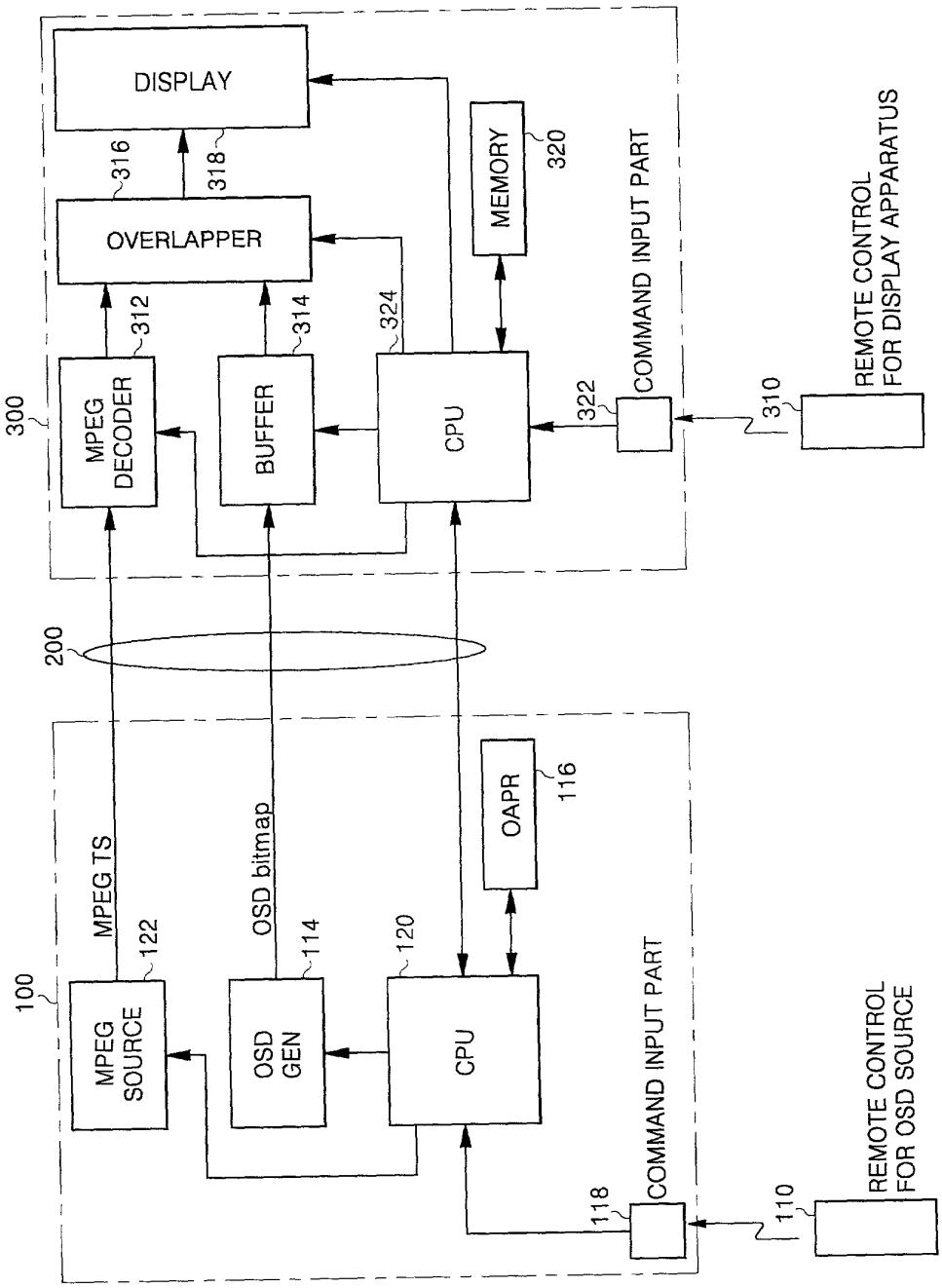
DISPLAY
316
318
OVERLAPPER
MEMORY
320
312
314
324
MPEG DECODER
BUFFER
CPU
322
COMMAND INPUT PART
310
REMOTE CONTROL FOR DISPLAY APPARATUS
300
200
OAPR
116
MPEG TS
OSD bitmap
122
114
120
100
MPEG SOURCE
OSD GEN
CPU
COMMAND INPUT PART
118
110
REMOTE CONTROL FOR OSD SOURCE

FIG. 3

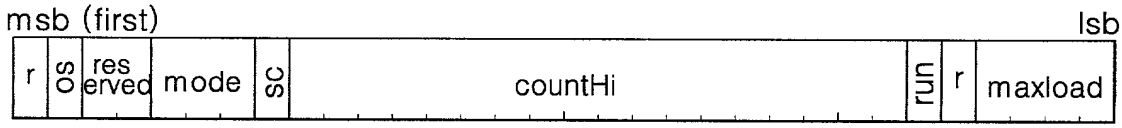

FIG. 4

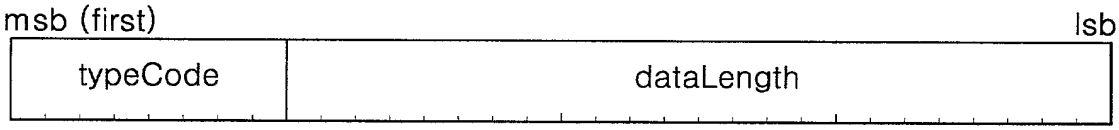
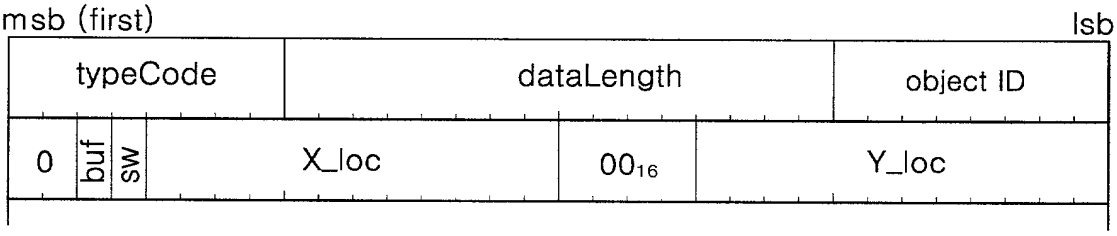
FIG. 5